

Minutes of TMAG Tool Committee Meeting, 4/24/08

Minutes by Scott Davidson

Attendees

Scott Davidson

Craig DePaul

Louis Ungar

The Survey

Looks good so far.

The next step is to market the survey, so that we can get it filled out.

The Liaison committee is responsible for this activity.

Scott will put link into main page, and make the full TMAG Beta web page the default page, so people will see it when they go to tmag4dft.org or tmag4dft.com

Question: How to display results?

The two simplest methods are a link to an Excel spreadsheet or an html table, but these are not searchable. A database would be useful. We have access to Cold Fusion, which the Besttest site uses. Scott will look through the Besttest site for useful applications, which might have to be modified for TMAG.

Discussion: How can we produce high level testability guidelines.

Craig and Scott agreed that the need is for a process, not specific design requirements.

First – the company must recognize that there is a problem

Second – there must be a high level testability and diagnosability strategy

Third – this strategy must be driven down to subcontractors, suppliers, and designers such that the components of the system support the high level strategy.

All these decisions must be made for the long run, understanding that suppliers will change.

One of the issues is when a component is a commodity part, and is not designed to support the system level strategy. How to produce a system level strategy with commodity parts? Some companies are doing this well, and some aren't.

Can we add a chapter on system level diagnosis to the surface mount testability guidelines?

Conclusion: the output of our work must be a set of processes for considering testability. Craig said that there are already books (Program-level internal documents) on this (the process and architecture for implementing design for Testability on large-scale integrated systems). How can we generalize this enough to be useful to a wide set of system suppliers in both the commercial and defense realms? The requirements at this level have to be driven down to suppliers. How can the requirements both ensure that subcomponents will contribute to system level testability and allow design freedom. Finally, what metrics can be used to measure compliance with DFT guidelines, to make this not

just a matter of judgment?